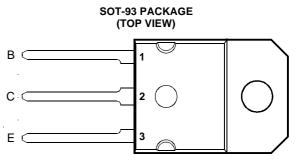
JULY 1968 - REVISED MARCH 1997

- Designed for Complementary Use with the TIP33 Series
- 80 W at 25°C Case Temperature
- 10 A Continuous Collector Current
- 15 A Peak Collector Current
- Customer-Specified Selections Available



Pin 2 is in electrical contact with the mounting base.

MDTRAA

absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING			VALUE	UNIT	
	TIP34		-80		
Collector base veltage $(I_{-} 0)$	TIP34A	V	-100	V	
Collector-base voltage ($I_E = 0$)	TIP34B	V _{CBO}	-120	v	
	TIP34C		-140		
Collector-emitter voltage ($I_B = 0$)	TIP34		-40		
	TIP34A	N/	-60	V	
	TIP34B	V _{CEO}	-80	v	
	TIP34C		-100		
Emitter-base voltage	V _{EBO}	-5	V		
Continuous collector current			-10	A	
Peak collector current (see Note 1)			-15	A	
Continuous base current			-3	A	
Continuous device dissipation at (or below) 25°C case temperature (see Note 2)			80	W	
Continuous device dissipation at (or below) 25°C free air temperature (see Note 3)			3.5	W	
Unclamped inductive load energy (see Note 4)			62.5	mJ	
Operating junction temperature range			-65 to +150	°C	
Storage temperature range	T _{stg}	-65 to +150	°C		
Lead temperature 3.2 mm from case for 10 seconds			250	°C	

NOTES: 1. This value applies for $t_p \leq 0.3$ ms, duty cycle $\leq 10\%.$

2. Derate linearly to 150°C case temperature at the rate of 0.64 W/°C.

3. Derate linearly to 150°C free air temperature at the rate of 28 mW/°C.

4. This rating is based on the capability of the transistor to operate safely in a circuit of: L = 20 mH, $I_{B(on)}$ = -0.4 A, R_{BE} = 100 Ω , $V_{BE(off)}$ = 0, R_S = 0.1 Ω , V_{CC} = -20 V.

PRODUCT INFORMATION

Information is current as of publication date. Products conform to specifications in accordance with the terms of Power Innovations standard warranty. Production processing does not necessarily include testing of all parameters.



JULY 1968 - REVISED MARCH 1997

electrical characteristics at 25°C case temperature

PARAMETER		TEST CONDITIONS			MIN	TYP	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C = -30 mA (see Note 5)		TIP34	-40			
			I _B = 0	TIP34A	-60			V
				TIP34B	-80			v
				TIP34C	-100			
		V _{CE} = -80 V	$V_{BE} = 0$	TIP34		-0.4		
	Collector-emitter	V _{CE} = -100 V	V _{BE} = 0 V _{BE} = 0	TIP34A			-0.4	mA
ICES	cut-off current	V _{CE} = -120 V		TIP34B			-0.4	ШA
		V _{CE} = -140 V	$V_{BE} = 0$	TIP34C			-0.4	
I	Collector cut-off	V _{CE} = -30 V	I _B = 0	TIP34/34A			-0.7	mA
ICEO	current	$V_{CE} = -60 V$	I _B = 0	TIP34B/34C			-0.7	ШA
	Emitter cut-off	V _{EB} = -5 V	I _C = 0				-1	mA
I _{EBO}	current	v _{EB} = -5 v	$I_{\rm C} = 0$				-1	
h _{FE}	Forward current	$V_{CE} = -4 V$	I _C = -1 A	(see Notes 5 and 6)	40			
11FE	transfer ratio	$V_{CE} = -4 V$	I _C = -3 A		20		100	
Varia	Collector-emitter	I _B = -0.3 A	I _C = -3 A	(see Notes 5 and 6)			-1	V
V _{CE(sat)}	saturation voltage	I _B = -2.5 A	I _C = -10 A				-4	v
Var	Base-emitter	V _{CE} = -4 V	I _C = -3 A	(see Notes 5 and 6)			-1.6	V
V_{BE}	voltage	$V_{CE} = -4 V$	I _C = -10 A				-3	v
h _{fe}	Small signal forward	V _{CE} = -10 V	-10 V I _C = -0.5 A	f = 1 kHz	20			
	current transfer ratio	VCE - 10 V	1C = 10.0 A	$C = -0.0 \Lambda$ $I = I RIZ$	20			
h _{fe}	Small signal forward	V _{CE} = -10 V	I _C = -0.5 A f = 1 MHz	f – 1 MHz	3			
	current transfer ratio	VCE - 10 V		5				

NOTES: 5. These parameters must be measured using pulse techniques, t_p = 300 $\mu s,$ duty cycle \leq 2%.

6. These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

thermal characteristics

PARAMETER			TYP	MAX	UNIT
$R_{\theta JC}$	Junction to case thermal resistance			1.56	°C/W
R _{θJA}	Junction to free air thermal resistance			35.7	°C/W

resistive-load-switching characteristics at 25°C case temperature

	PARAMETER	TEST CONDITIONS [†]			MIN	ТҮР	MAX	UNIT
t _{on}	Turn-on time	I _C = -6 A	I _{B(on)} = -0.6 A	$I_{B(off)} = 0.6 A$		0.4		μs
t _{off}	Turn-off time	$V_{BE(off)} = 4 V$	$R_L = 5 \Omega$	t_p = 20 µs, dc \leq 2%		0.7		μs

[†] Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.

JULY 1968 - REVISED MARCH 1997

TYPICAL DC CURRENT GAIN COLLECTOR-EMITTER SATURATION VOLTAGE vs vs **COLLECTOR CURRENT BASE CURRENT** TCS634AA TCS634AB 1000 -10 $V_{\text{CE(sat)}}$ - Collector-Emitter Saturation Voltage - V V_{CF} = -4 V -1 A $T_c = 25^{\circ}C$ -3 A $t_n = 300 \ \mu s$, duty cycle < 2% -6 A h_{FE} - DC Current Gain 100 -1-0 10 -0-1 1 -0-01 -0-01 -0-1 -1-0 -10 -0-01 -0-1 -1-0 -10 I_B - Base Current - A I_c - Collector Current - A Figure 1. Figure 2.

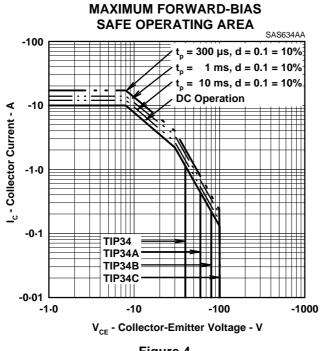
TYPICAL CHARACTERISTICS

BASE-EMITTER VOLTAGE VS COLLECTOR CURRENT 1-6 -1-4



Power D

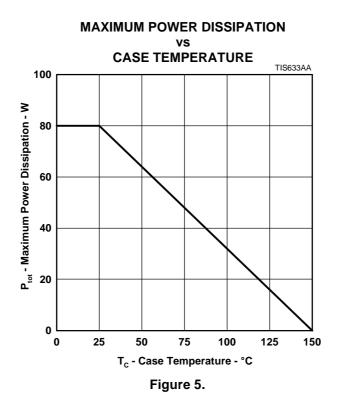
JULY 1968 - REVISED MARCH 1997



MAXIMUM SAFE OPERATING REGIONS







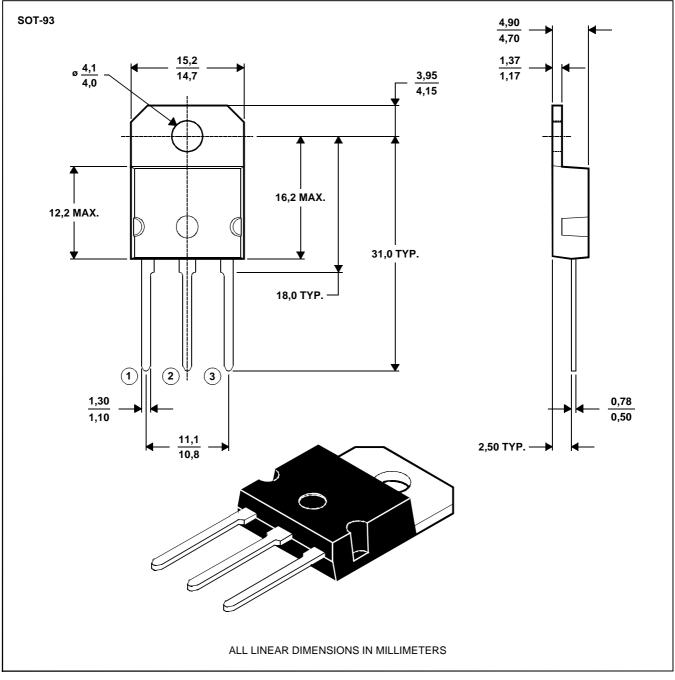
JULY 1968 - REVISED MARCH 1997

MECHANICAL DATA

SOT-93

3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.





MDXXAW



JULY 1968 - REVISED MARCH 1997

IMPORTANT NOTICE

Power Innovations Limited (PI) reserves the right to make changes to its products or to discontinue any semiconductor product or service without notice, and advises its customers to verify, before placing orders, that the information being relied on is current.

PI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with PI's standard warranty. Testing and other quality control techniques are utilized to the extent PI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except as mandated by government requirements.

PI accepts no liability for applications assistance, customer product design, software performance, or infringement of patents or services described herein. Nor is any license, either express or implied, granted under any patent right, copyright, design right, or other intellectual property right of PI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used.

PI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, INTENDED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT APPLICATIONS, DEVICES OR SYSTEMS.

Copyright © 1997, Power Innovations Limited