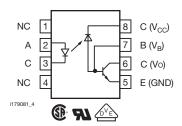




High Speed Optocoupler, 1 MBd, Photodiode with Transistor Output





DESCRIPTION

The 6N135 and 6N136 are optocouplers with a GaAlAs infrared emitting diode, optically coupled with an integrated photo detector which consists of a photo diode and a high-speed transistor in a DIP-8 plastic package.

Signals can be transmitted between two electrically separated circuits up to frequencies of 2 MHz. The potential difference between the circuits to be coupled should not exceed the maximum permissible reference voltages.

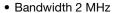
FEATURES

Isolation test voltages: 5300 V_{RMS}



• High bit rates: 1 Mbit/s

• High common-mode interference immunity



Open-collector output

• External base wiring possible

 Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

Pb-free



RoHS COMPLIANT

AGENCY APPROVALS

- UL1577, file no. E52744 system code H double protection
- DIN EN 60747-5-2 (VDE0884)/DIN EN 60747-5-5 (pending), available with option 1
- CSA 93751

ORDERING INFORMATION					
		DIP-8 Option 6			
6 N 1 3 #	PACKAGE OPTION	TAPE 7.62 mm 10.16 mm			
		AND REEL Option 7 Option 9			
AGENCY CERTIFIED/PACKAGE	CTR	R (%)			
UL, CSA	≥7	≥ 19			
DIP-8	6N135	6N136			
DIP-8, 400 mil, option 6	-	6N136-X006			
SMD-8, option 7	6N135-X007T ⁽¹⁾	6N136-X007T ⁽¹⁾			
SMD-8, option 9	-	6N136-X009T ⁽¹⁾			
VDE, UL, CSA	≥7	≥ 19			
DIP-8	-	6N136-X001			
	- 6N136-X016				
DIP-8, 400 mil, option 6	-	6N136-X016			
DIP-8, 400 mil, option 6 SMD-8, option 7	- 6N135-X017T ⁽¹⁾	6N136-X016 6N136-X017T			

Note

⁽¹⁾ Also available in tubes; do not add T to end

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)										
PARAMETER TEST CONDITION SYMBOL VALUE UNIT										
INPUT										
Reverse voltage		V _R	5	V						
Forward current		I _F	25	mA						
Peak forward current	t = 1 ms, duty cycle 50 %	I _{FSM}	50	mA						
Maximum surge forward current	t ≤ 1 µs, 300 pulses/s		1	Α						
Thermal resistance		R _{th}	700	K/W						
Power dissipation	T _{amb} = 70 °C	P _{diss}	45	mW						

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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
OUTPUT								
Supply voltage		V _S	- 0.5 to 15	V				
Output voltage		V _O	- 0.5 to 15	V				
Emitter base voltage		V_{EBO}	5	V				
Output current		I _O	8	mA				
Maximum output current			16	mA				
Base current		Ι _Β	5	mA				
Thermal resistance			300	K/W				
Power dissipation	T _{amb} = 70 °C	P _{diss}	100	mW				
COUPLER								
Isolation test voltage between emitter and detector	t = 1 s	V _{ISO}	5300	V_{RMS}				
Pollution degree (DIN VDE 0109)			2					
Isolation resistance	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω				
Isolation resistance	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω				
Storage temperature range		T _{stg}	- 55 to + 125	°C				
Ambient temperature range		T _{amb}	- 55 to + 100	°C				
Soldering temperature (1)	max. ≤ 10 s, dip soldering ≥ 0.5 mm from case bottom	T _{sld}	260	°C				

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT									
Forward voltage	I _F = 16 mA		V_{F}		1.33	1.9	V		
Breakdown voltage	$I_R = 10 \mu A$		V_{BR}	5			V		
Reverse current	V _R = 5 V		I _R		0.5	10	μA		
Capacitance	$V_R = 0 V, f = 1 MHz$		Co		30		pF		
Temperature coefficient, forward voltage	I _F = 16 mA		$\Delta V_F/\Delta T_A$		- 1.7		mV/°C		
OUTPUT									
Logic low supply current	$I_F = 16 \text{ mA}, V_O = \text{open},$ $V_{CC} = 15 \text{ V}$		I _{CCL}		150		μΑ		
Logic high supply current	$I_F = 0$ mA, $V_O = $ open, $V_{CC} = 15$ V		I _{CCH}		0.01	1	μΑ		
Output valtage output low	$I_F = 16 \text{ mA}, I_O = 1.1 \text{ mA},$ $V_{CC} = 4.5 \text{ V}$	6N135	V _{OL}		0.1	0.4	V		
Output voltage, output low	$I_F = 16 \text{ mA}, I_O = 2.4 \text{ mA}, V_{CC} = 4.5 \text{ V}$	6N136	V _{OL}		0.1	0.4	V		
Output ourront output high	$I_F = 0 \text{ mA}, V_O = V_{CC} = 5.5 \text{ V}$		I _{OH}		3	500	nA		
Output current, output high	$I_F = 0 \text{ mA}, V_O = V_{CC} = 15 \text{ V}$		I _{OH}		0.01	1	μΑ		
COUPLER	COUPLER								
Capacitance (input to output)	f = 1 MHz		C _{IO}		0.6		pF		

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.



CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)									
PARAMETER TEST CONDITION PART SYMBOL MIN. TYP. MAX. UNIT									
Current transfer ratio	$I_F = 16 \text{ mA}, V_O = 0.4 \text{ V},$	6N135	CTR	7	16		%		
	$V_{CC} = 4.5 V$	6N136	CTR	19	35		%		
	$I_F = 16 \text{ mA}, V_O = 0.5 \text{ V},$	6N135	CTR	5			%		
	$V_{CC} = 4.5 V$	6N136	CTR	15			%		

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION PART SYMBOL MIN. TYP. MAX. UNIT							
High to low	$I_F = 16 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 4.1 \text{ k}\Omega$	6N135	t _{PHL}		0.3	1.5	μs	
	$I_F = 16 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1.9 \text{ k}\Omega$	6N136	t _{PHL}		0.2	0.8	μs	
Law to bigh	$I_F = 16 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 4.1 \text{ k}\Omega$	6N135	t _{PLH}		0.3	1.5	μs	
Low to high	$I_F = 16 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1.9 \text{ k}\Omega$	6N136	t _{PLH}		0.2	0.8	μs	

COMMON MODE TRANSIENT IMMUNITY (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	PARAMETER TEST CONDITION PART SYMBOL MIN. TYP. MAX. UNIT							
High	$I_F = 0 \text{ mA}, V_{CM} = 10 V_{P-P}, V_{CC} = 5 V, R_L = 4.1 k\Omega$	6N135	CM _H		1000		V/µs	
	$I_F = 0 \text{ mA}, V_{CM} = 10 V_{P-P}, V_{CC} = 5 V, R_L = 1.9 k\Omega$	6N136	CM _H		1000		V/µs	
Law	$I_F = 16 \text{ mA}, V_{CM} = 10 V_{P-P}, V_{CC} = 5 V, R_L = 4.1 \text{ k}\Omega$	6N135	CM _L		1000		V/µs	
Low	$I_F = 16 \text{ mA}, V_{CM} = 10 V_{P-P}, V_{CC} = 5 V, R_L = 1.9 \text{ k}\Omega$	6N136	CM _L		1000		V/µs	

SAFETY AND INSULATION RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Climatic classification (according to IEC 68 part 1)				55/100/21				
Comparative tracking index		CTI	175		399			
V _{IOTM}			8000			V		
V _{IORM}			890			V		
P _{SO}					500	mW		
I _{SI}					300	mA		
T _{SI}					175	°C		
Creepage distance	Standard DIP-8		7			mm		
Clearance distance	Standard DIP-8		7			mm		
Creepage distance	400 mil DIP-8		8			mm		
Clearance distance	400 mil DIP-8		8			mm		

Note

According to DIN EN 60747-5-2 (VDE 0884), this optocoupler is suitable for "safe electrical insulation" only within the safety ratings.
 Compliance with the safety ratings shall be ensured by means of protective circuits.



TYPICLA CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

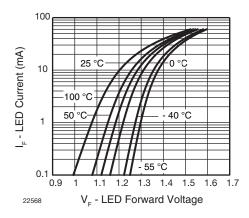


Fig. 1 - LED Forward Current vs. Forward Voltage

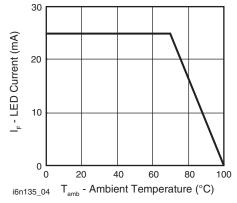


Fig. 2 - Permissible Forward LED Current vs. Temperature

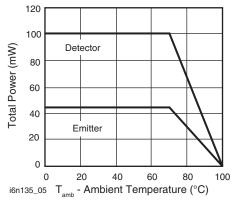


Fig. 3 - Permissible Power Dissipation vs. Temperature

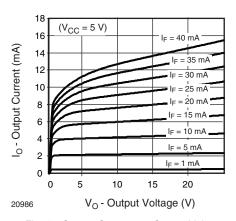


Fig. 4 - Output Current vs. Output Voltage

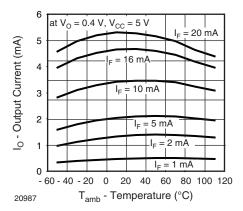


Fig. 5 - Output Current vs. Temperature

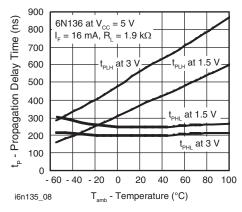


Fig. 6 - Propagation Delay vs. Ambient Temperature

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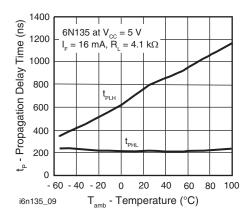


Fig. 7 - Propagation Delay vs. Ambient Temperature

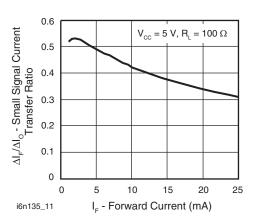


Fig. 9 - Small Signal Current Transfer Ratio vs. Quiescent Input Current

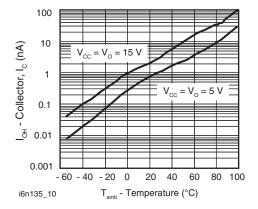


Fig. 8 - Logic High Output Current vs. Temperature

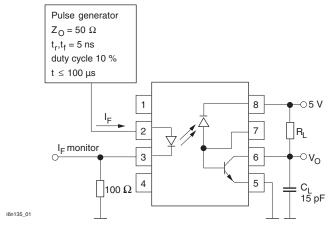
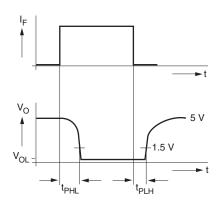


Fig. 10 - Switching Times





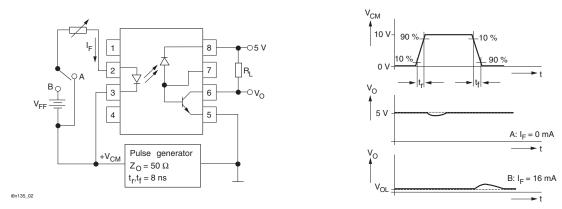
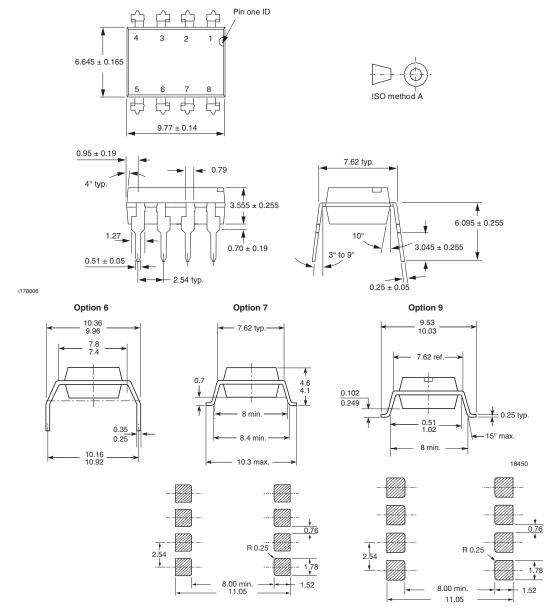


Fig. 11 - Common-Mode Interference Immunity

PACKAGE DIMENSIONS in millimeters





PACKAGE MARKING



Notes

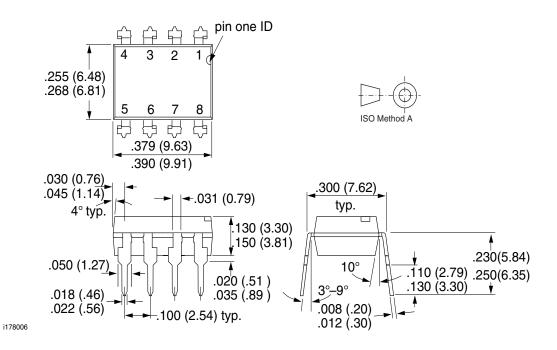
- Only options 1, and 7 are reflected in the package marking.
- The VDE logo is only marked on option 1 parts.
- Tape and reel suffix (T) is not part of the package marking.







Package Dimensions in Inches (mm)





Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operatingsystems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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